

# Viva Voce In Electrical Engineering By Dk Sharma

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### Viva Voce In Electrical Engineering

#### **PRACTICAL / VIVA VOCE**

ETIC 203 Instrumentation Engineering - I 2 0 2 ETIC 205 Electrical Machines 3 1 4 ETIC 207 Computer Programming & Computation Technique 3 1 4 ETIC 209 Analog Electronics 3 1 4 ETIC 211 Industrial Instruments - I 3 1 4 PRACTICAL / VIVA VOCE ETIC 251 Circuits & Systems Lab 0 2 1 ETIC 253 Computer Lab 0 2 1

#### **SARDAR VALLABHBHAI NATIONAL INSTITUTE OF ...**

Interested faculty members, research scholars, students and staff are welcomed to attend the viva-voce examination A copy of the thesis would be made available for reference in the Institute Central Library and the Department library Dr XYZ Supervisor(s) Professor/Associate/Assistant Professor Electrical Engineering Department SVNIT Copy to:

#### **Department of Electrical and Electronic Engineering ...**

Department of Electrical and Electronic Engineering Chittagong University of Engineering and Technology Eligible candidates' list of MSc Engg/M Engg for VIVA VOCE Created Date:

#### **FACULTY OF ENGINEERING AND TECHNOLOGY DEPARTMENT ...**

for viva-voce examination at the end of eighth semester 12 Industrial Training (Value added courses) One credit courses shall be offered by a Department with the prior approval from the Dean, Faculty of Engineering and Technology For one credit course, a relevant potential

#### **[www.vssut.ac.in](http://www.vssut.ac.in)**

Electrical Engineering Department shall defend his PhD thesis before the Viva-Voce Board consisting of all DRC members and External Examiner on 09112019 at 1100 AM in the Electrical Department Seminar Hall VSSUT, Burla Thesis Title: "OPTIMAL OPERATION OF ...

#### **W.B.S.C.T.E. TEACHING AND EXAMINATION SCHEME FOR ...**

TEACHING AND EXAMINATION SCHEME FOR DIPLOMA COURSES COURSE NAME: ELECTRICAL ENGINEERING COURSE CODE : EE 7 General

Viva voce 100 2 Total 14 18 40 80 120 280 275 125 24 Surjit Singh Electrical Engineering Drawing SKKataria & Sons EXAMINATION SCHEME (THEORITICAL)

### **ELEC9453 Project Part C - engineering.unsw.edu.au**

School of Electrical Engineering and Telecommunications Term 3, 2019 Course Outline ELEC9453 Project Part C The marking of the Open Day presentation is based on the quality of the Project poster, viva voce, and demonstration of the project work Only the supervisor will assess the participation effort The assessment of the report and Open Day

### **Design and Implementation of a Differentiated Service ...**

Department of Electronics and Electrical Communication Engineering G S SANYAL SCHOOL OF TELECOMMUNICATIONS INDIAN INSTITUTE OF TECHNOLOGY, KHARAGPUR, INDIA February 2010 and that the student has successfully defended the thesis in the viva-voce ...

### **W.B.S.C.T.E. TEACHING AND EXAMINATION SCHEME FOR ...**

COURSE NAME: ELECTRICAL ENGINEERING COURSE CODE : EE DURATION OF COURSE : 6 SEMESTER 15, Viva-voce - 10 West Bengal State Council of Technical Education (A Statutory Body under West Bengal Act XXI of 1995) Kolkata Karigori Bhavan, 2nd Floor, 110 S N Banerjee Road, Kolkata - 700 013

### **SCHEME OF EXAMINATION and SYLLABI for Electrical ...**

1 Modified Scheme and Syllabus of B Tech-EE (1st Semester to 8th Semester) implemented from Academic Session wef 2015-16, approved in the 23rd thBOS and 40 AC meeting of USET SCHEME OF EXAMINATION and SYLLABI for Bachelor of Technology Electrical Engineering

### **ELECTRONICS AND COMMUNICATION ENGINEERING**

ELECTRONICS AND COMMUNICATION ENGINEERING FROM 2009 ADMISSION ONWARDS CALICUT UNIVERSITY (PO), THENHIPALAM EC09 306 Electrical Engineering 3 1 - 30 70 3 4 EC09 307(P) Digital Electronics Lab - - 3 50 50 3 2 Viva Voce - - - - 100 - 3 TOTAL 12 4 14 28 ELECTIVES EC09 L06 Soft Computing

### **Statuten der Doctoral School of Electrical Engineering**

Furthermore, the Doctoral School of Electrical Engineering recommends a trial presentation to be held prior to the viva voce The date and time for this trial run must be communicated to the Doctoral School of Electrical Engineering as well as to the students allocated to this school The dates and times of the viva voce must be

### **JAMMU AND KASHMIR STATE POWER DEVELOPMENT ...**

I BTech/BE in Civil Engineering with at least 60% marks from a University established by law in India/ AICET approved BE/BTech Electrical Engineering / BE/BTech Electronics & Communication Engineering 5 Junior Engineer Viva voce 10 (ten) points For Geological Assistant

### **Electrical and Electronics Engineering**

01 Civil Engineering 02 Electrical and Electronics Engineering 03 Mechanical Engineering 04 Electronics and Communication Engineering 05 Computer Science and Engineering viva-voce, and Project Work shall be evaluated for 100, 100, 100 and 200 marks respectively ii

### **New Curriculum for Four-Year Bachelor of Electrical ...**

New Curriculum for Four-Year Bachelor of Electrical Engineering Course Department of Electrical Engineering 1st Year 1st Semester Code Subject Pds / week Credit Marks L S Exam Sessional EE/S/415 GENERAL VIVA VOCE 0 0 3 100 Sub- Total 14 12 500 400 Total 26 900 ...

### **ELECTRICAL AND ELECTRONICS ENGINEERING (Part Time)**

Syllabus- Part Time B Tech Electrical & Electronics Engg SCHEME AND SYLLABI FOR THIRD TO EIGHTH SEMESTERS OF BACHELOR OF TECHNOLOGY IN ELECTRICAL AND ELECTRONICS ENGINEERING (Part Time) FROM 2009 ADMISSION ONWARDS CALICUT UNIVERSITY (PO), THENHIPALAM University of Calicut 2 Viva Voce - - - 100 - - 3 Total 8 4 8 28 University of

### **ELECTRICAL CIRCUITS LABORATORY LAB MANUAL**

INSTITUTE OF AERONAUTICAL ENGINEERING (Autonomous) Dundigal, Hyderabad - 500 043 ELECTRICAL AND ELECTRONICS ENGINEERING Program Outcomes PO1 Engineering knowledge: Apply the knowledge of mathematics, science, engineering fundamentals, and an engineering specialization to the solution of complex engineering problems

### **ELEC4121 Thesis Part B - UNSW Faculty of Engineering**

School of Electrical Engineering and Telecommunications , 2019 Course Outline ELEC4121 Thesis Part B 1 Course Staff Thesis supervisor: same staff who supervised the student in Thesis Part A The marking of the Open Day presentation is based on the quality of the thesis poster, viva voce, and

### **SEMINAR TALKS - PH**

Sreevastan S, Research Scholar, Department of Electrical Engineering (EE16S062) will give a seminar talk on Simultaneous Detection of Defect and Lift-off using a Modified Pulsed Eddy Current Probe at 1600 hours on June 20, 2019 in the Seminar Hall (ESB 244) The PhD Viva Voce Examination of Dinesh K, Research Scholar, Department

### **Electrical & Electronics Engineering Full Syllabus**

electrical and electronics engineering a theory sl no code theory contact periods per week l t p total credits 1 eee 501 electrical machines-i 3 1 0 4 4  
2 ee 504 power electronics 3 0 0 3 3 3 ee 503 control system 3 1 0 4 4 4 eee 502 electromagnetic theory 3 1 0 4 4 5 ei 502 microprocessor & micro controller